

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L2	20	(motorola same intel) same (multiplexed near3 bus)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:14
2	BRS	L3	2	(multiplexed near3 bus) and ((motorola same intel) and (switch\$3 adj logic))	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:15
3	BRS	L4	0	1 and 2	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:15
4	BRS	L5	0	((motorola same intel) same (network adj switch)) same (compatibility or compatible)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:16
5	BRS	L6	0	((motorola same intel) same (network adj switch))	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:16
6	BRS	L7	126	((motorola same intel) and (network adj switch))	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:16

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L8	31	(motorola same intel).ti,ab.	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:17
8	BRS	L1	73	((motorola same intel) and (network adj switch)) and (compatibility or compatible)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/09/07 15:26